

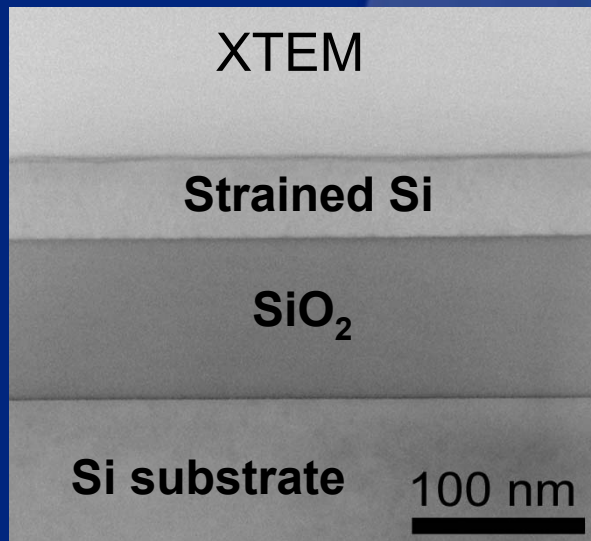
Advanced SiGe-free Strained Si on Insulator Substrates: Thermal Stability and Carrier Mobility Enhancement

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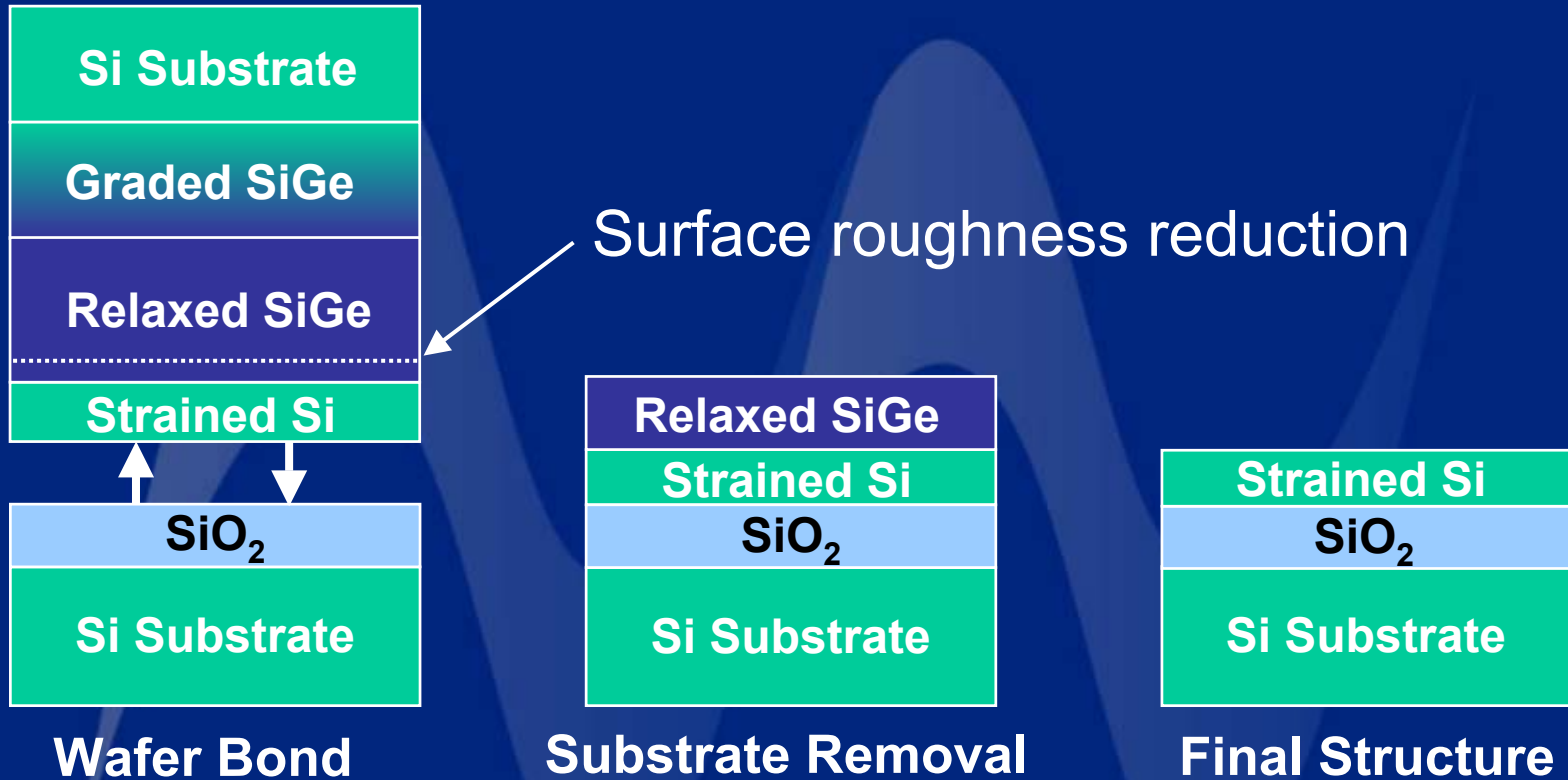
AmberWave Systems, Salem, NH 03079 USA

- Motivation
- SSOI substrate fabrication process
- Recent improvements
- SSOI substrate data
 - Uniformity
 - Surface roughness
 - Strain thermal stability
 - First mobility enhancement demonstration by HgFET
- Summary

Strained Si On Insulator

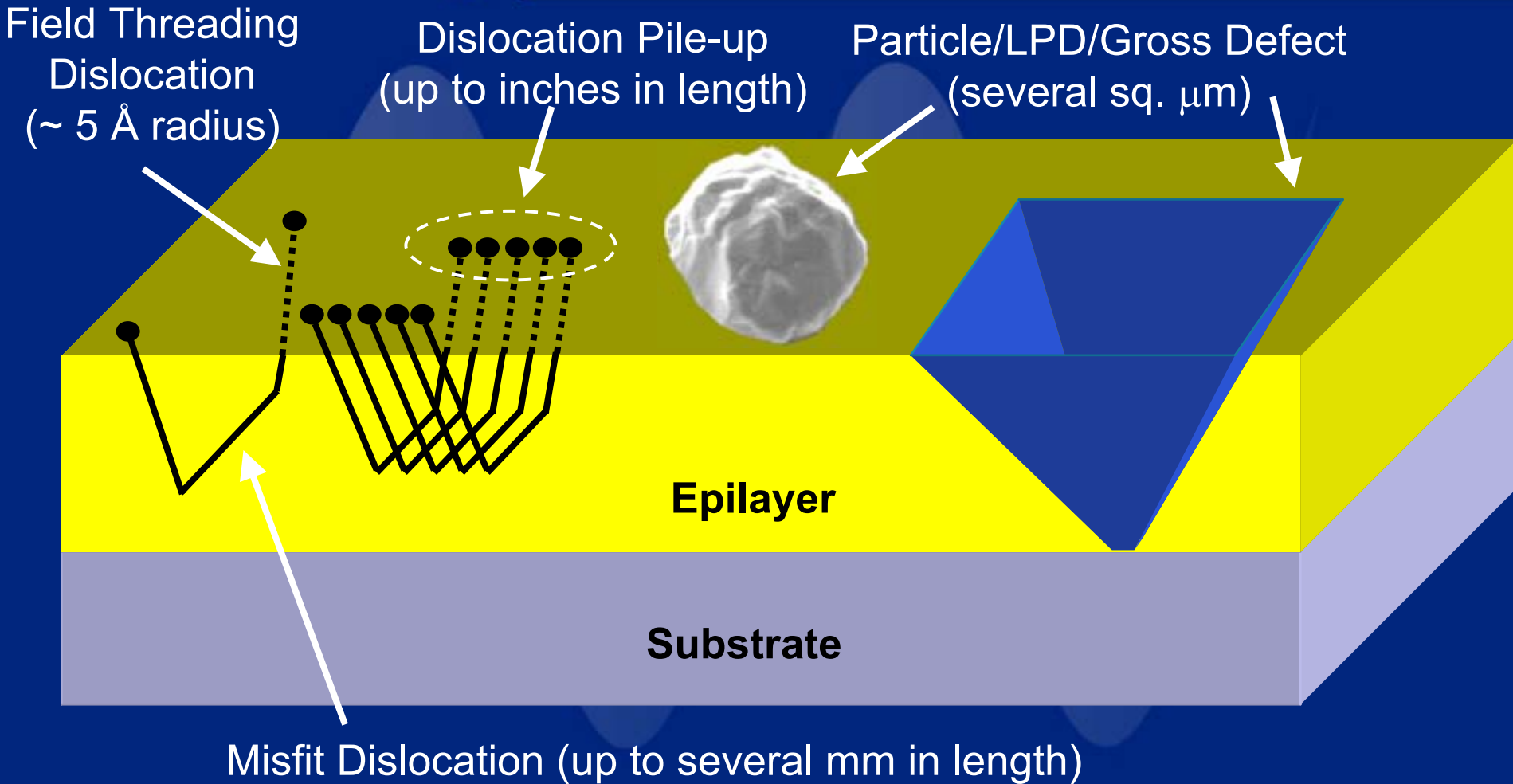


- Combination of 2 high-performance technologies: Strained Si and SOI
- SiGe-free structure
 - 20% SSOI, $\varepsilon = 0.8\%$
- Strained Si layer defined epitaxially
- Capable of very thin structures $<200 \text{ \AA}$



Starting material quality critical to process

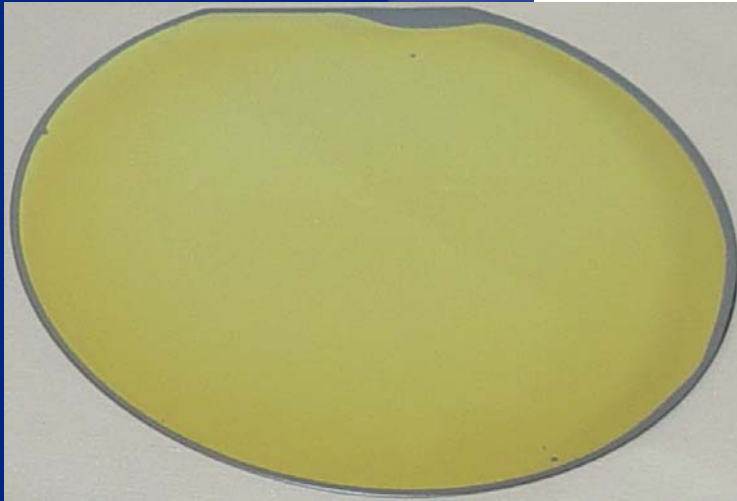
Bulk Strained Si Substrate Defects Transferred to SSOI



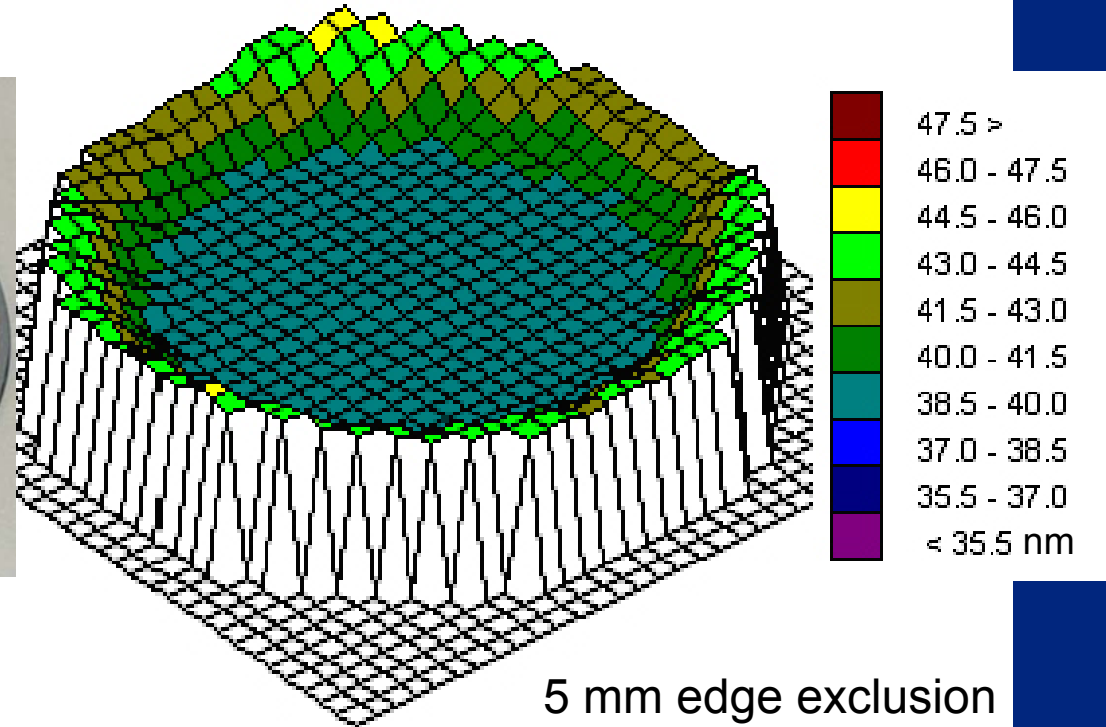
- **Bond yield**
 - Minimization of epi induced particles and pileups
 - Optimization of wafer bonding clean and anneal steps
- **Film uniformity**
 - Improved SiGe compositional uniformity
 - Improved epi strained Si thickness uniformity
 - SiGe removal by combination oxidation/wet-etch
- **Surface roughness**
 - SiGe removal by combination oxidation/wet-etch

Uniformity = $\pm 7.9\%$, $\sigma = 5.8\%$

150 mm wafer

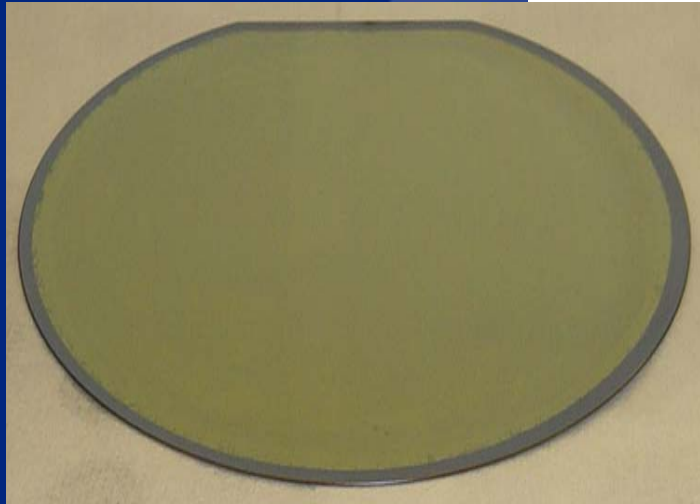


100 nm BOX

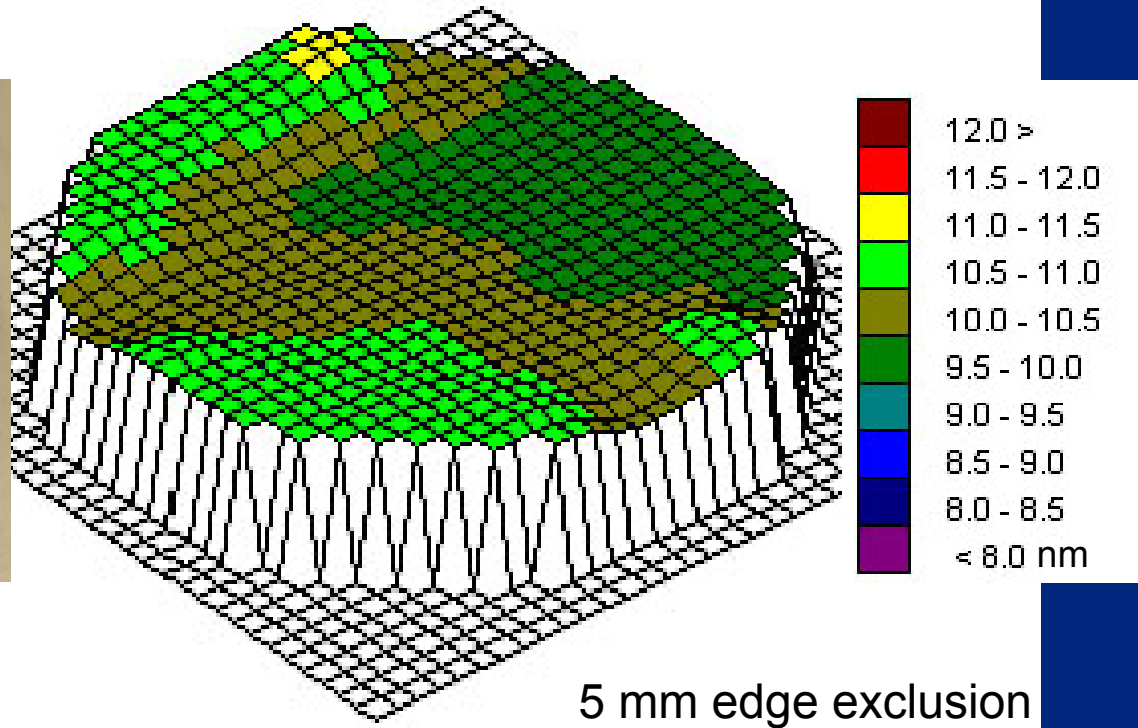


Uniformity $\pm 7.12\%$, $\sigma = 4.6\%$

150 mm wafer

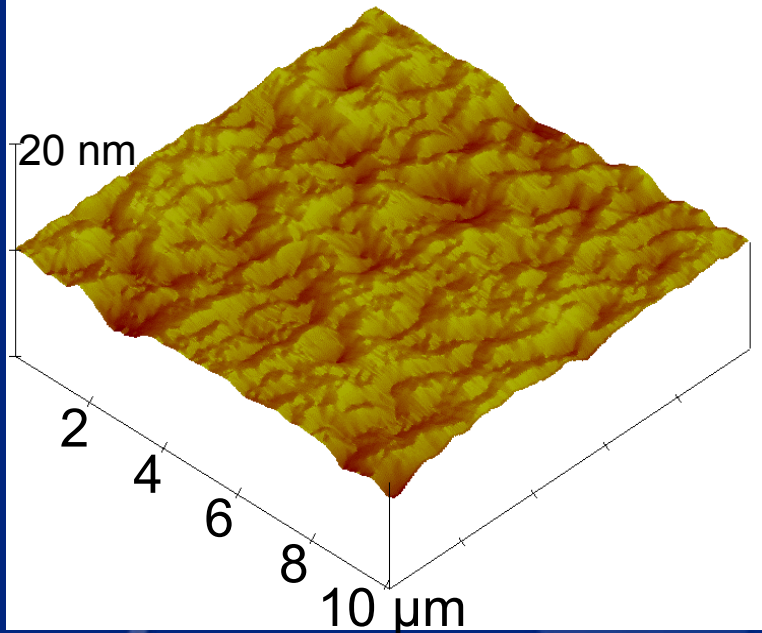


200 nm BOX



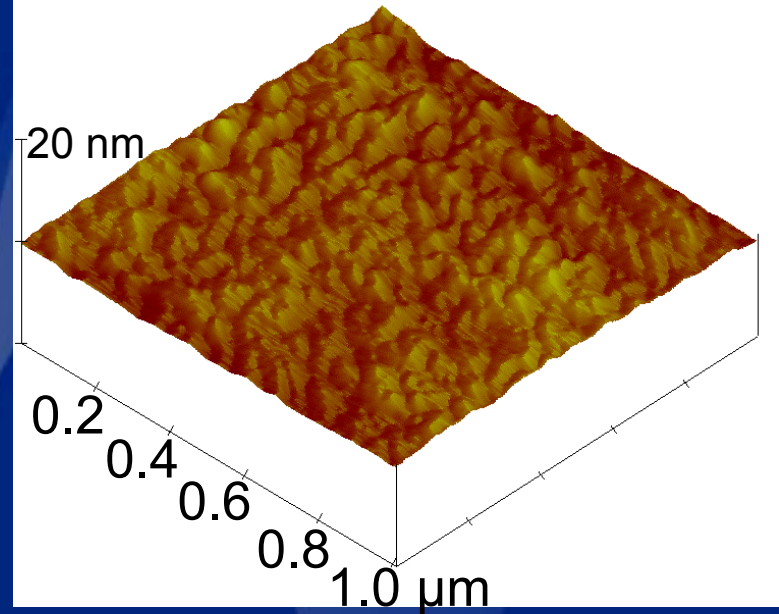
Ultrathin SSOI films achieved

10x10 μm^2 AFM image



0.72 nm RMS

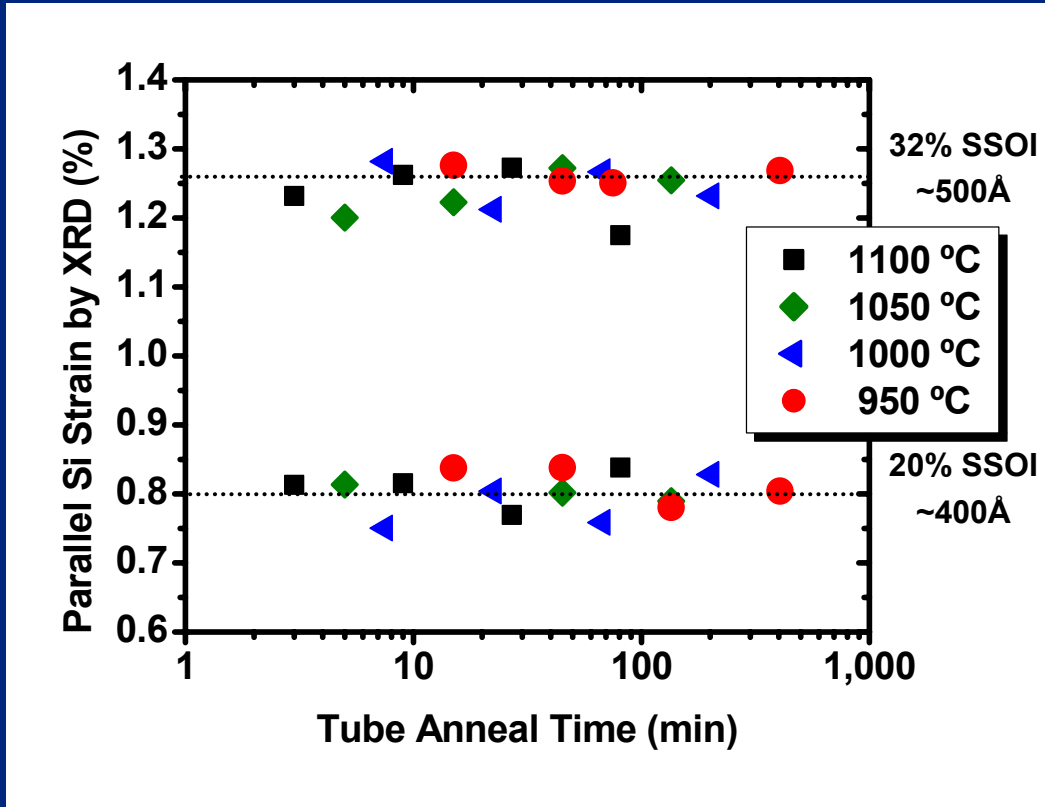
1x1 μm^2 AFM image



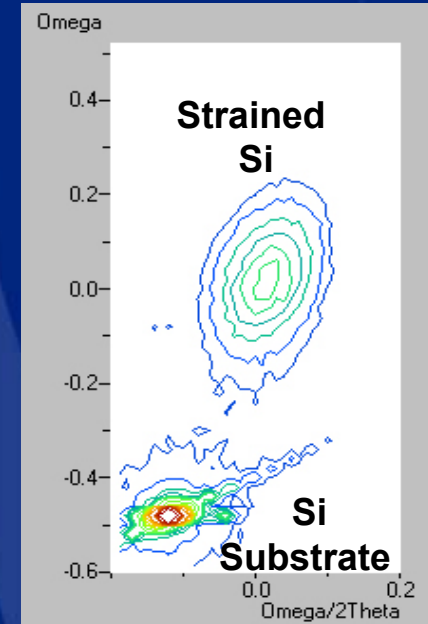
0.49 nm RMS

Cross-hatch is not observed

Strain Thermal Stability: Measured by XRD (verified by Raman)

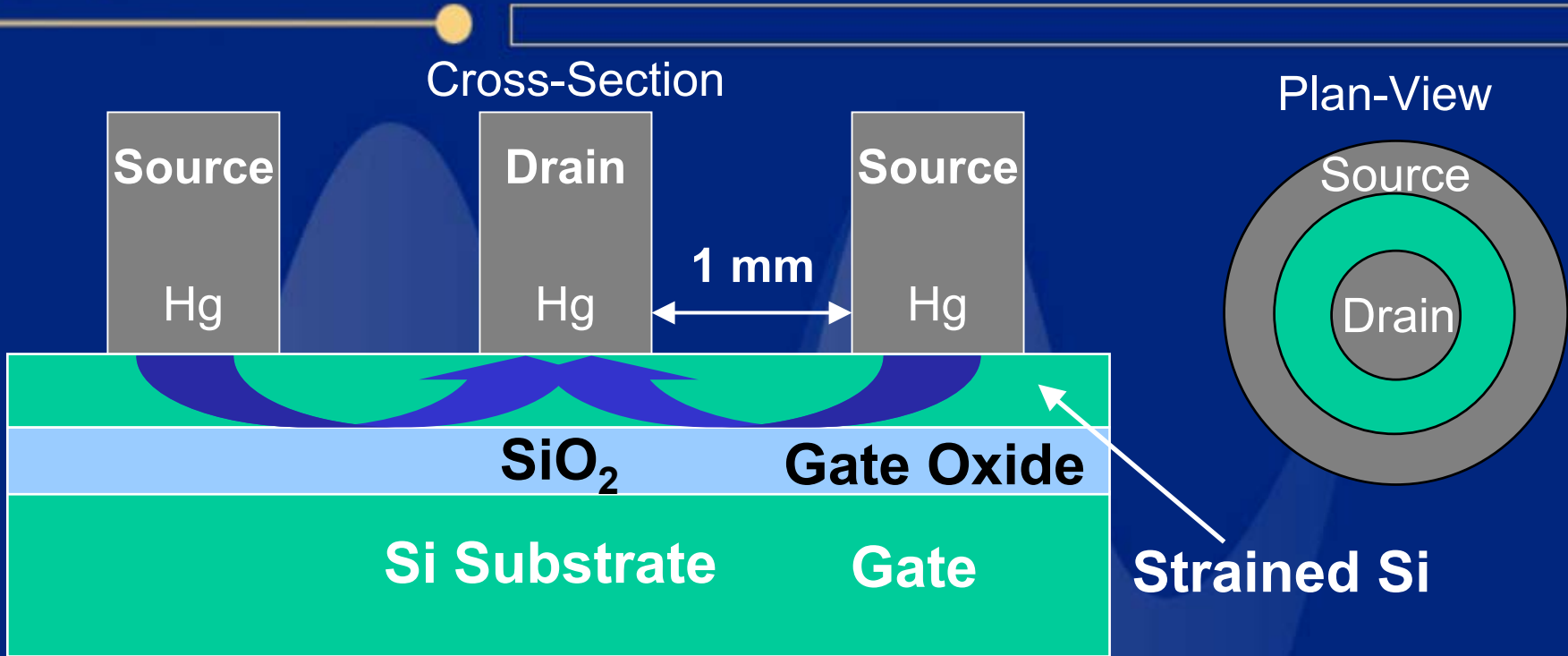


224GI RSM
32% SSOI 1050°C 45 min



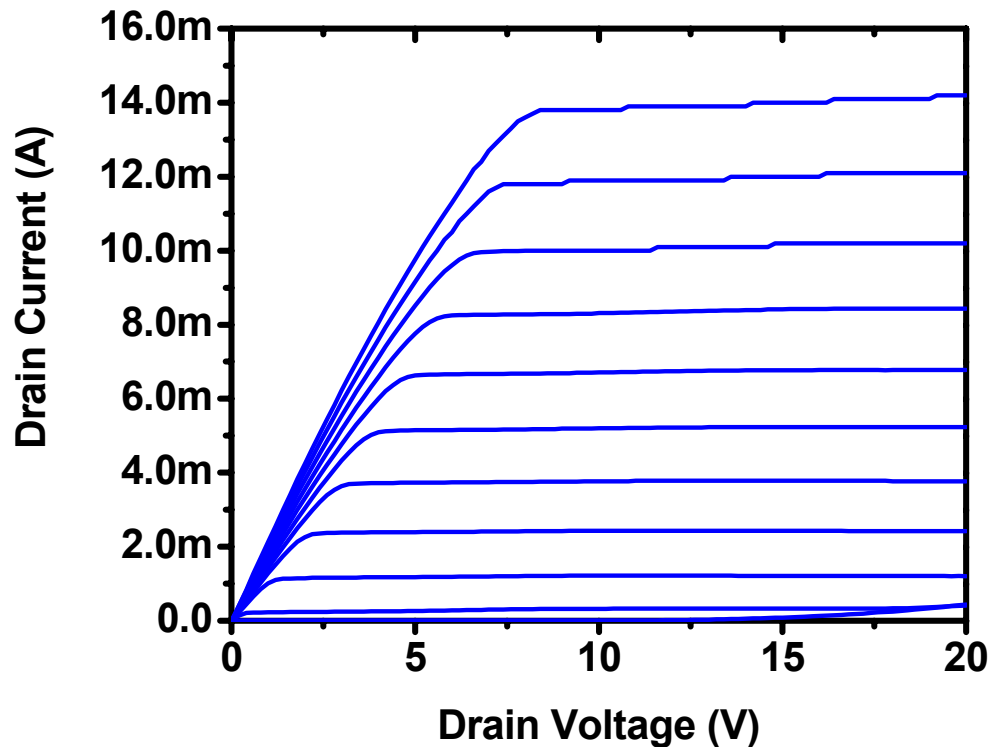
-M. Erdtmann, et al., SSDM p.290(2003)

Tensile strain maintained during 1100°C anneals for thick films



-H. J. Hovel, Solid State Elec. **47**, 1311 (2003)

- Common SOI characterization technique to measure mobility, DITs, etc.
- Easy Set up
- Large concentric area technique > 10 mm²



- Extracted SSOI mobility
902 cm^2/Vsec
- SOI mobility
600-650 cm^2/Vsec
- DITs comparable to SOI

-H. J. Hovel, Solid State Elec. **47**, 1311 (2003)

40-50% Electron Mobility Improvement over SOI

- **SSOI advancements**
 - Thickness variation of $< 8\%$ on ultrathin 100 \AA films
 - Surface roughness $< 5 \text{ \AA}$
 - Improved thermal stability with $> 1.2\%$ strain unaffected by annealing up to 1100°C
- **First SSOI electron mobility enhancement demonstrated**
 - 40-50% enhancement over SOI

Contributions from Arthur Pitera and Dr. Yuri Erokhin are gratefully acknowledged